## Switchmode Series NPN Silicon Power Transistors (4A / 400V / 75W)

## FEATURES

- $\mathrm{V}_{\text {CEO(SUS) }} \geq 400 \mathrm{~V} @ \mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$
- $\mathrm{V}_{\mathrm{CE}(\mathrm{sat})}=1.0 \mathrm{~V}$ (Max.) $@ \mathrm{I}_{\mathrm{C}}=4 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=1 \mathrm{~A}$
- Switching time $-\mathrm{t}_{\mathrm{f}}=0.9 \mu \mathrm{~s}(\mathrm{Max}). @ \mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}$
- 700V blocking capability


INTERNAL SCHEMATIC DIAGRAM

(NPN)

| ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ unless otherwise specified) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| SYMBOL | PARAMETER |  | VALUE | UNIT |
| $\mathrm{V}_{\text {CEV }}$ | Collector to base voltage ( $\mathrm{V}_{\mathrm{BE}}=0$ ) |  | 700 | V |
| $\mathrm{V}_{\text {CEO }}$ | Collector to emitter voltage ( $\mathrm{I}_{\mathrm{B}}=0$ ) |  | 400 |  |
| $\mathrm{V}_{\text {Ebo }}$ | Emitter to base voltage |  | 9 |  |
| Ic | Collector current - continuous |  | 4 | A |
| $\mathrm{I}_{\text {CM }}$ | Peak collector current (Note 1) |  | 8 |  |
| $\mathrm{I}_{\mathrm{B}}$ | Base current - continuous |  | 2 |  |
| $I_{\text {BM }}$ | Peak base current (Note 1) |  | 4 |  |
| $\mathrm{I}_{\mathrm{E}}$ | Emitter current - continuous |  | 6 |  |
| $\mathrm{I}_{\text {EM }}$ | Peak emitter current (Note 1) |  | 12 |  |
| $\mathrm{P}_{\mathrm{D}}$ | Total power dissipation | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | 75 | w |
|  | Derate above $25^{\circ} \mathrm{C}$ |  | 0.6 | W/ ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{j}}$ | Junction temperature |  | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {stg }}$ | Storage temperature |  | -65 to 150 |  |
| $\mathrm{T}_{\mathrm{L}}$ | Maximum lead temperature for soldering purposes: $1 / 16^{\prime \prime}$ from case for $\leq 10$ seconds |  | 265 | ${ }^{\circ} \mathrm{C}$ |

Note: 1. Pulse test : Pulse width $=5 \mathrm{~ms}$, duty cycle $\leq 10 \%$

SEMICONDUCTOR

| SYMBOL | PARAMETER | VALUE | UNIT |
| :---: | :---: | :---: | :---: |
| $\mathrm{R}_{\mathrm{th}(\mathrm{j} \text { c) }}$ | Maximum thermal resistance, junction to case | 1.67 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\text {th( }}$-a) | Maximum thermal resistance, junction to ambient | 62.5 . |  |


| ELECTRICAL CHARACTERISTICS ( $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ unless otherwise specified) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYMBOL | PARAMETER | CONDITIONS | MIN | TYP | MAX | UNIT |
| O OFF CHARACTERISTICS |  |  |  |  |  |  |
| $I_{\text {cev }}$ | Collector cutoff current | $\mathrm{V}_{\text {CE }}=700 \mathrm{~V}, \mathrm{~V}_{\text {BE(off) }}=1.5 \mathrm{~V}$ |  |  | 1.0 | mA |
|  |  | $\mathrm{V}_{\mathrm{CE}}=700 \mathrm{~V}, \mathrm{~V}_{\mathrm{BE} \text { (off) }}=1.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{C}}=100^{\circ} \mathrm{C}$ |  |  | 5.0 |  |
| $\mathrm{I}_{\text {ceo }}$ | Collector cutoff current | $\mathrm{V}_{\mathrm{CE}}=400 \mathrm{~V}, \mathrm{I}_{\mathrm{B}}=0$ |  |  | 0.1 |  |
| $\mathrm{I}_{\text {ebo }}$ | Emitter cutoff current | $\mathrm{V}_{\text {EBO }}=9 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |  |  | 1.0 |  |
| $\mathrm{V}_{\text {CEO(SUS) }}{ }^{*}$ | Collector to emitter sustaining voltage | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | 400 |  |  |  |
| $\mathrm{V}_{\text {(BR)CEV }}$ | Collector to base breakdown voltage | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{BE}}=0$ | 700 |  |  | v |
| $\mathrm{V}_{\text {(BR) }{ }^{\text {ebo }} \text { ( }}$ | Emitter to base breakdown voltage | $\mathrm{I}_{\mathrm{E}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{C}}=0$ | 9 |  |  |  |
| O ON CHARACTERISTICS |  |  |  |  |  |  |
| $\mathrm{h}_{\text {FE }}$ | Forward current transfer ratio (DC current gain) | $\mathrm{I}_{\mathrm{C}}=1 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=5 \mathrm{~V}$ | 10 |  | 60 |  |
|  |  | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=5 \mathrm{~V}$ | 8 |  | 40 |  |
| $\mathrm{V}_{\mathrm{CE} \text { (sat)* }}$ | Collector to emitter saturation voltage | $\mathrm{I}_{\mathrm{C}}=1 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.2 \mathrm{~A}$ |  |  | 0.5 | V |
|  |  | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~A}$ |  |  | 0.6 |  |
|  |  | $\mathrm{I}_{\mathrm{C}}=4 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=1 \mathrm{~A}$ |  |  | 1.0 |  |
|  |  | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{C}}=100^{\circ} \mathrm{C}$ |  |  | 1.0 |  |
| $V_{\text {BE(on) }}{ }^{*}$ | Base to emitter on voltage | $\mathrm{I}_{\mathrm{C}}=1 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.2 \mathrm{~A}$ |  |  | 1.2 | V |
|  |  | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~A}$ |  |  | 1.6 |  |
|  |  | $\mathrm{I}_{\mathrm{C}}=2 \mathrm{~A}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~A}, \mathrm{~T}_{\mathrm{C}}=100^{\circ} \mathrm{C}$ |  |  | 1.5 |  |
| O DYNAMIC CHARACTERISTICS |  |  |  |  |  |  |
| $\mathrm{f}_{\mathrm{T}}$ | Transition frequency (Current gain- Bandwidth product) | $\mathrm{I}_{\mathrm{C}}=0.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{f}_{\text {test }}=1 \mathrm{MHz}$ | 4 |  |  | MHz |
| $\mathrm{C}_{\text {ob }}$ | Output capacitance | $\mathrm{V}_{\mathrm{CB}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0, \mathrm{f}_{\text {test }}=0.1 \mathrm{MHz}$ |  | 65 |  | pF |
| O SWITCHING CHARACTERISTICS |  |  |  |  |  |  |
| $t_{d}$ | Delay time | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=125 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=2 \mathrm{~A} \\ & \mathrm{I}_{\mathrm{B} 1}=\mathrm{I}_{\mathrm{B} 2}=0.4 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=25 \mu \mathrm{~s} \\ & \text { duty clcye } \leq 1 \% \end{aligned}$ |  | 0.03 | 0.1 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\mathrm{r}}$ | Rise time |  |  | 0.35 | 0.7 |  |
| $\mathrm{t}_{\text {s }}$ | Storage time |  |  | 2.0 | 4.0 |  |
| $\mathrm{t}_{\mathrm{f}}$ | Fall time |  |  | 0.45 | 0.9 |  |

[^0]Nell High Power Products

Fig. 1 DC current gain


Fig. $3 \mathrm{~V}_{\mathrm{BE}}-\mathrm{I}_{\mathrm{C}}$ characteristics (Typical)


Fig. 5 Collector cutoff region


Fig. $2 \mathrm{~V}_{\mathrm{CE}(\text { sat })}-I_{\mathrm{B}}$ characteristics (Typical)


Fig. $4 \mathrm{~V}_{\mathrm{CE}(\mathrm{sat})}$ - $\mathrm{I}_{\mathrm{C}}$ characteristics (Typical)


Fig. 6 Capacitance


Fig. 7 Turn-On time


Fig. 8 Turn-Off time


Fig. 9 Test conditions for dynamic performance

| REVERSE BIAS SAFE OPERATING AREA AND INDUCTIVE SWITCHING |  | RESISTIVE SWITCHING |
| :---: | :---: | :---: |
|  | PW and $\mathrm{V}_{\mathrm{cc}}$ Adjusted for Desired $\mathrm{I}_{\mathrm{C}}$ RB Adjusted for Desired $I_{B 1}$ |  |
|  | Coil Data: GAP for $200 \mu \mathrm{H} / 20 \mathrm{~A}$ $\mathrm{~V}_{\text {cc }}=20 \mathrm{~V}$ <br> Ferroxcube Core \#6656 $\mathrm{L}_{\text {coil }}=200 \mu \mathrm{H}$ $\mathrm{V}_{\text {clamp }}=300 \mathrm{Vdc}$ <br> Full Bobbbin ( $\sim 16$ Turns)\#16   | $\begin{aligned} & \mathrm{V}_{\mathrm{CC}}=125 \mathrm{~V} \\ & \mathrm{R}_{\mathrm{C}}=62 \Omega \\ & \mathrm{D} 1=1 \mathrm{~N} 5820 \text { or Equiv. } \\ & \mathrm{R}_{\mathrm{B}}=22 \Omega \end{aligned}$ |
|  | $\mathrm{t}_{1}$ ADJUSTED TO OBTAIN IC <br> $\mathrm{t}_{1} \approx \frac{\mathrm{~L}_{\mathrm{coii}}\left(\mathrm{I}_{\mathrm{CKK}}\right)}{\mathrm{V}_{\mathrm{Cc}}}$ <br> Test Equipment Scope-Tektronics 475 or Equivalent $\mathrm{t}_{2} \approx \frac{\mathrm{~L}_{\mathrm{coil}}\left(\mathrm{I}_{\mathrm{CPK}}\right)}{\mathrm{V}_{\mathrm{clamp}}}$ | $\mathrm{t}_{\mathrm{r}}, \mathrm{t}_{\mathrm{f}}<10 \mathrm{~ns}$ <br> Duty Cycle $=1.0 \%$ <br> $R_{B}$ and $R_{C}$ adjusted for desired $\mathrm{I}_{\mathrm{B}}$ and $\mathrm{I}_{\mathrm{C}}$ |

SEMICONDUCTOR

Fig. 10 Typical thermal response $\left[Z_{\text {th(j-c) }}(t) 】\right.$


Fig. 11 Forward bias safe operating area
(FBSOA)

Collector-Emitter voltage, $\mathrm{V}_{\mathrm{CE}}(\mathrm{V})$

## FORWARD BIAS

There are two limitations on the power handling ability of a transistor:average junction temperature and second breakdown. Safe operating area curves indicate $\mathrm{I}_{\mathrm{C}}-\mathrm{V}_{\mathrm{CE}}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Fig. 11 is based on $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C} ; \mathrm{T}_{\mathrm{J}(\mathrm{pk})}$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to $10 \%$ but must be derated when $T_{C} \geq 25^{\circ} \mathrm{C}$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown on Fig. 11 may be found at any case temperature by using the appropriate curve on Fig. 13.
$\mathrm{T}_{\mathrm{J}(\mathrm{pk})}$ may be calculated from the data in Fig.10. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations mposed by second breakdown.

Fig. 12 Reverse bias switching safe operating area (RBSOA)


## REVERSE BLAS

For inductive loads, high voltage and high current must be sustained simultaneously during turn-off, in most cases, with the base to emitter junction reverse biased. Under these conditions the collector voltage must be held to a safe level at or below a specific value of collector current. This can be accomplished by several means such as active clamping, RC snubbing, load line shaping, etc. The safe level for these devices is specified as Reverse Bias Safe Operating Area and repesents the voltage-current conditions during reverse biased turn-off. This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode. Fig. 12 gives the complete RBSOA characteristics.

Fig. 13 Forward bias power derating


TO-220AB



[^0]:    *Pulsed : Pulse duration $=300 \mu \mathrm{~s}$, duty cycle $=2 \%$.

